

Docket No.: 49657-217



41  
PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of : Customer Number: 20277  
Shigeki OHBAYASHI : Confirmation Number: 8805  
Serial No.: 09/196,136 : Group Art Unit: 2133  
Filed: November 20, 1998 : Examiner: Guy Lamarre  
For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING BONDING OPTIONAL  
FUNCTION

**SUPPLEMENTAL RESPONSE**

Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RECEIVED**  
AUG 24 2004  
Technology Center 2100

Sir:

The following supplemental Remarks are submitted in further response to the Office Action dated May 13, 2004. A previous Amendment was submitted on August 13, 2004. Please enter the following Remarks as follows.

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